FIG. 1

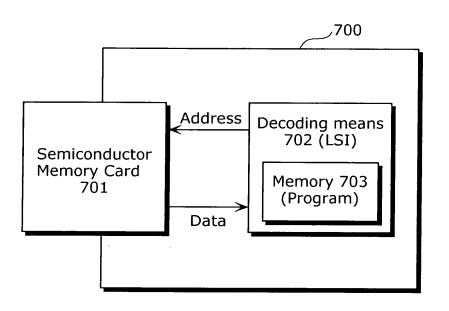
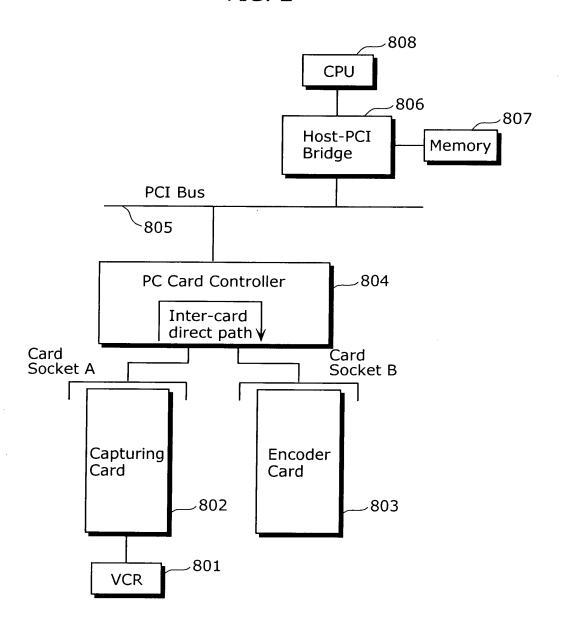


FIG. 2



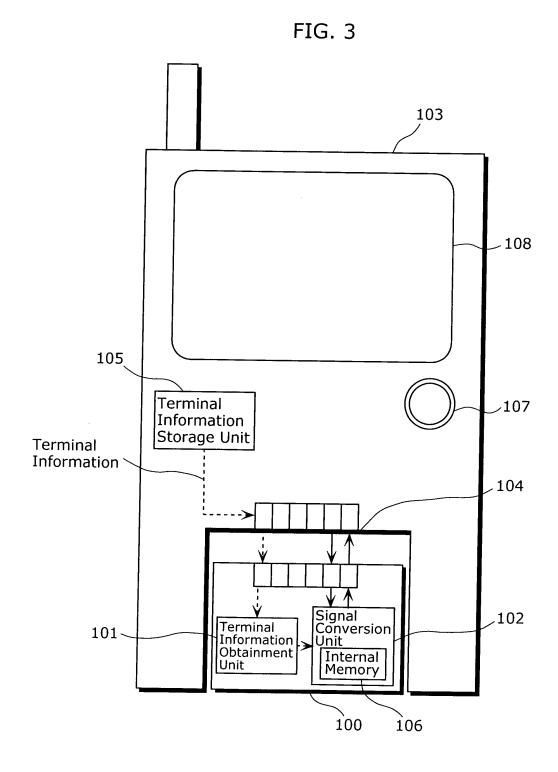


FIG. 4

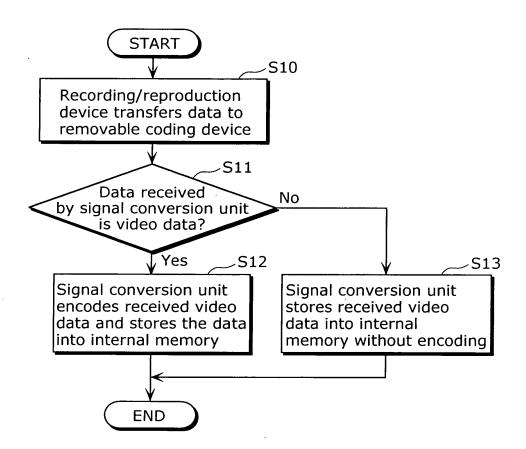
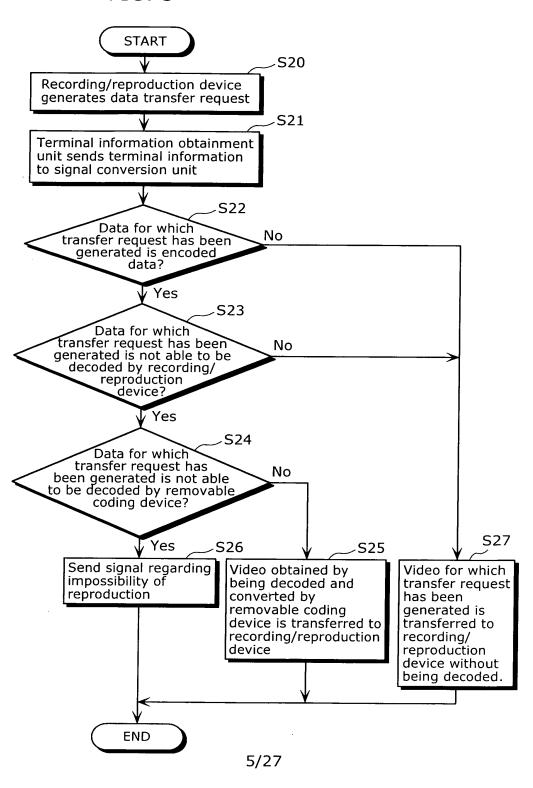
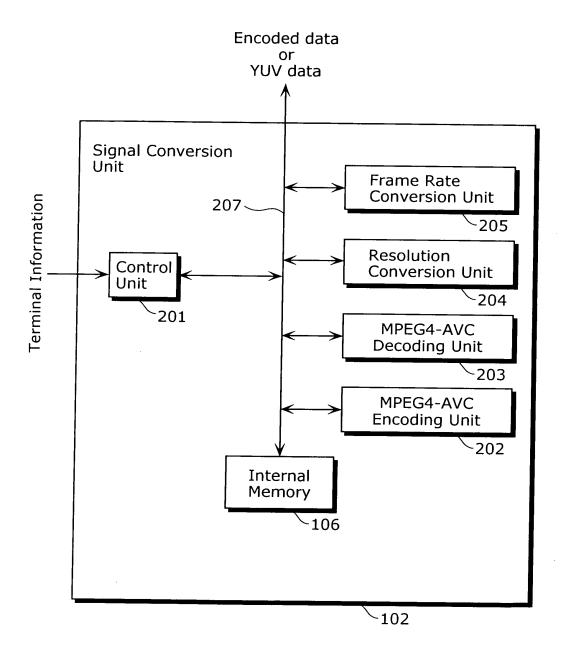


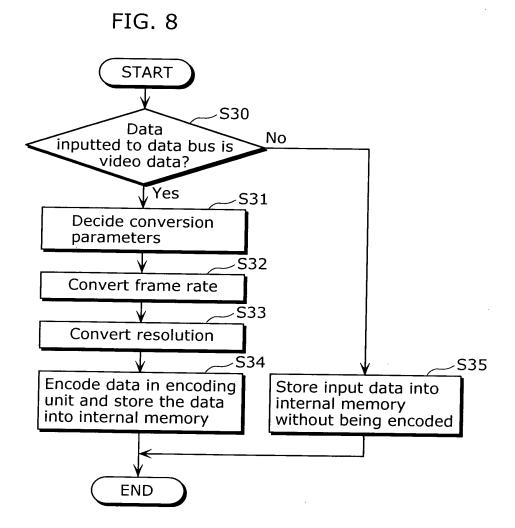
FIG. 5

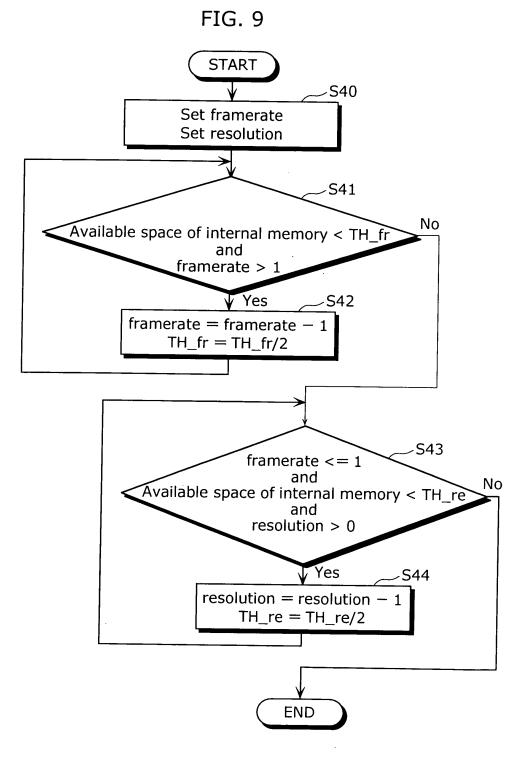


- Terminal Information					- Terminal Information Tab	Built-in Coding Device Information	Not built in	MPEG2	MPEG4-SP	MPEG4-ASP							
minal I		2	2	0	Termin	Bu					-						
Ter		nformation		ation		A	0		2	m							1
	Terminal Information	Maximum Data Transfer Amount Information	Resolution Information	Built-in Coding Device Information		Maximum Data Transfer Amount Information	2MByte/sec	4MByte/sec	8MByte/sec		Resolution Information	SubQCIF	QCIF	QVGA	VGA	SD	
		1aximur		Bui		Q	0	H	2		Ω	0	1	2	က	4	
FIG. 6A									· ((FIG. 6B							_;

FIG. 7









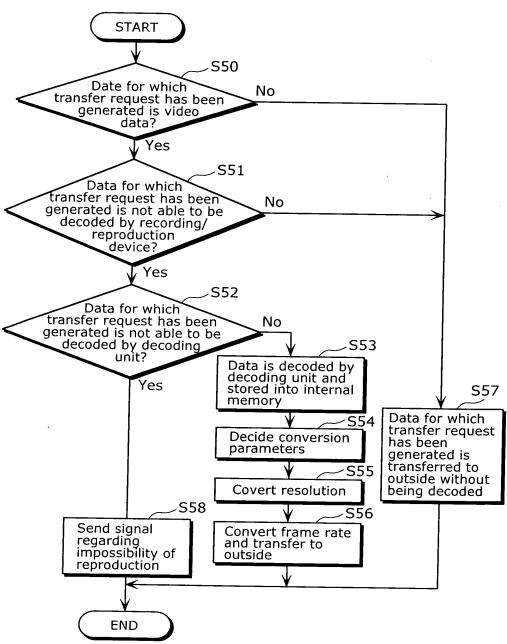


FIG. 11

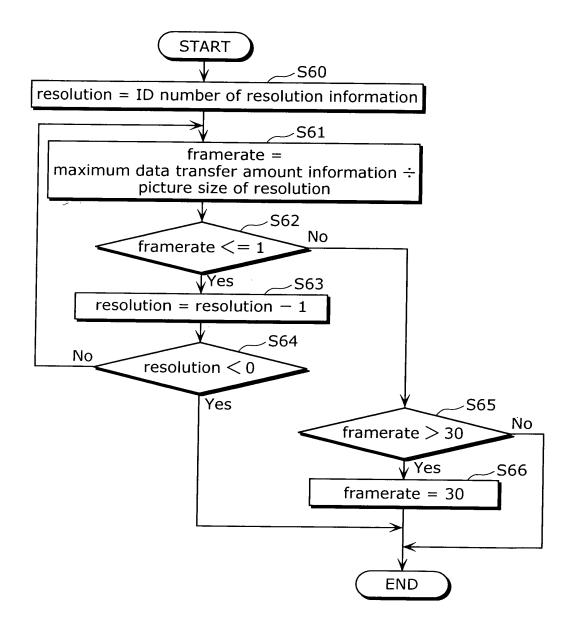


FIG. 12

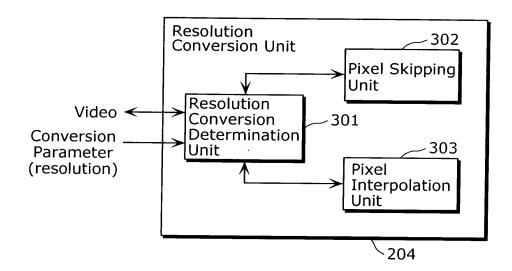


FIG. 13 **START** ·S70 org_resolution = resolution stored in video header ·S71 No resolution > org_resolution **V**Yes ·S72 pixel interpolation -S73 No resolution < org_resolution Yes S74 pixel skipping **END**

FIG. 14

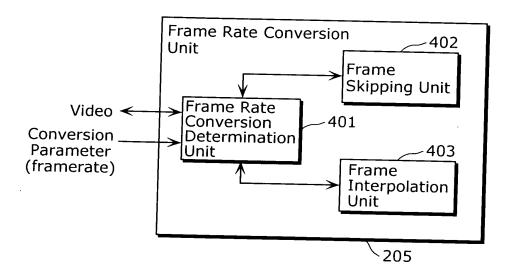


FIG. 15

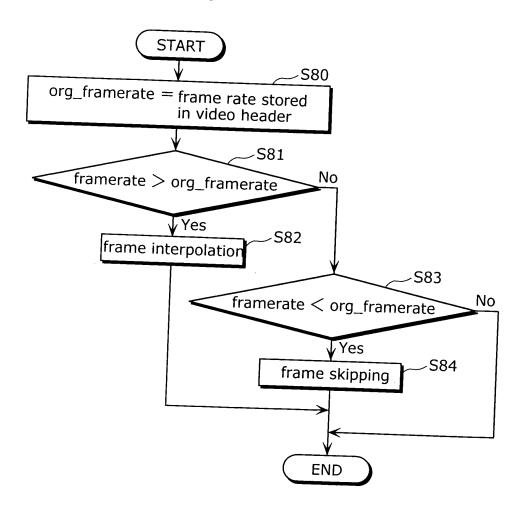
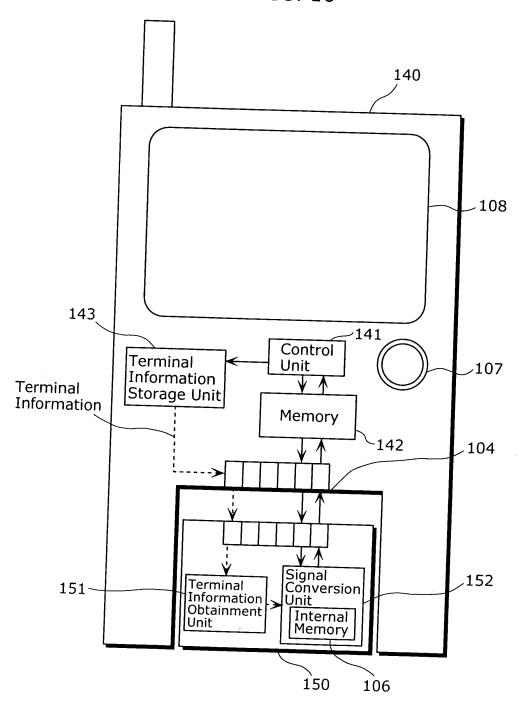


FIG. 16



- Terminal Information						Terminal Information Tab	Built-in Coding Device Information	Not built in	MPEG2	MPEG4-SP		MPEG4-ASP		Memory Available Space	~ 32MBvte	32 ~ 128MBvte	128 ~ 256MBvte	22 (21.12.12)	
erminal I	ΩI	2	2	0	2	erminal I	Built-i Device	Ž		2	2	Σ		Memory,	0	32	128		
T		mation		uC) T	Ω	0	П	2	~	,		ID	0	H	2		1
	tion	ant Infor	tion	formatic	pace				-		1		-					.	-
	Terminal Information	Maximum Data Transfer Amount Information	Resolution Information	Built-in Coding Device Information	Memory Available Space		Maximum Data Transfer Amount Information	2MByte/sec	4MByte/sec	8MByte/sec		Resolution Information		SubQCIF	QCIF	QVGA	VGA	SD	
		Maxi					Ω	0	П	7		ID				7	3	4	
	FIG. 17A							FIG. 17B											

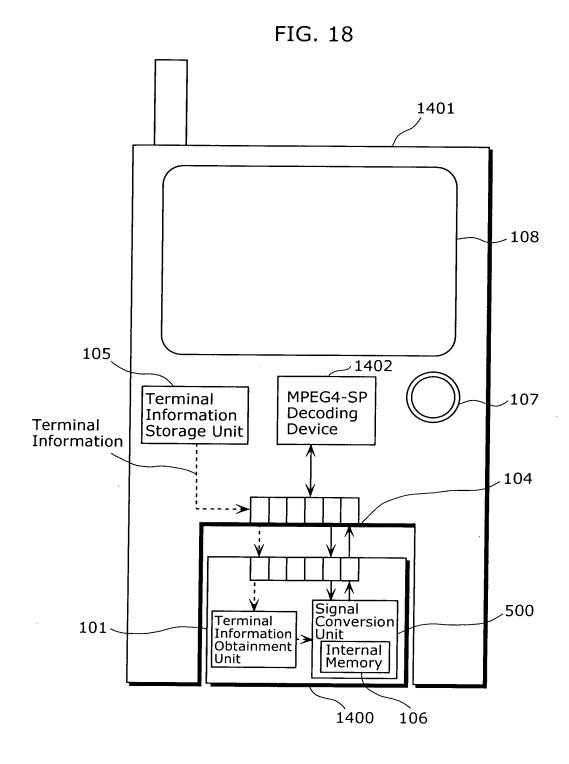


FIG. 19

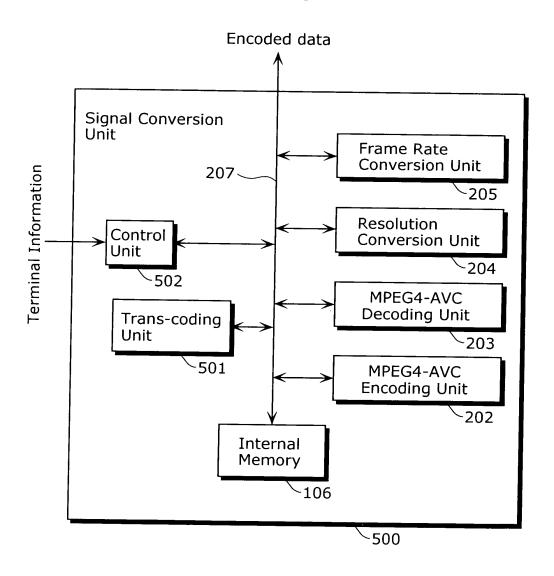
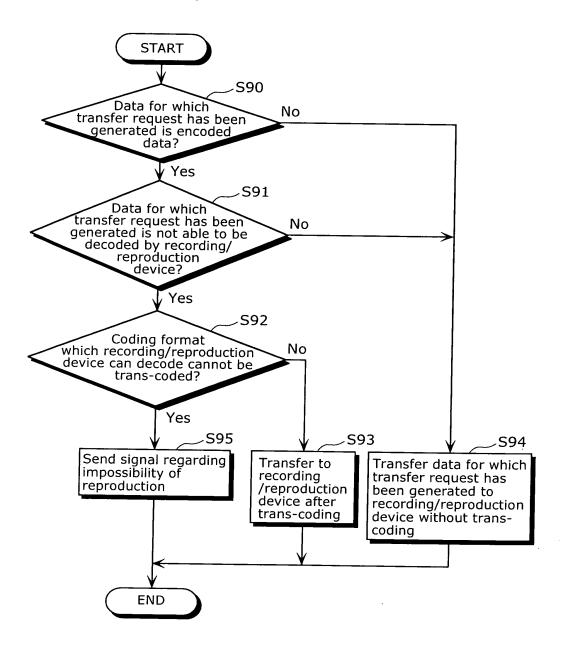


FIG. 20





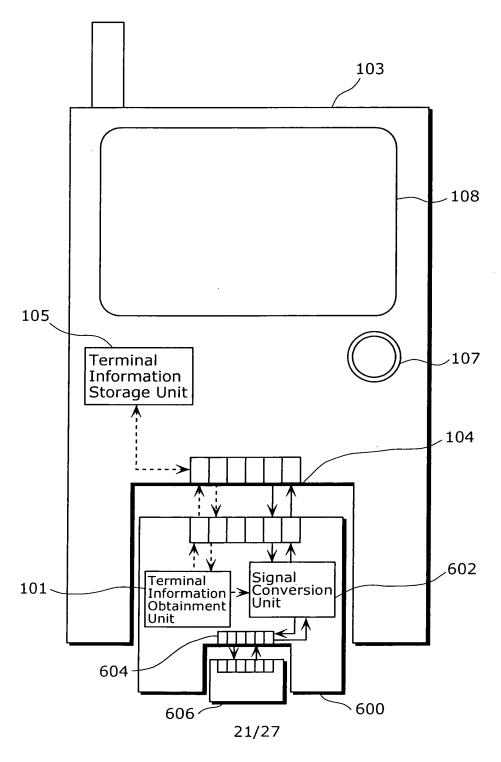
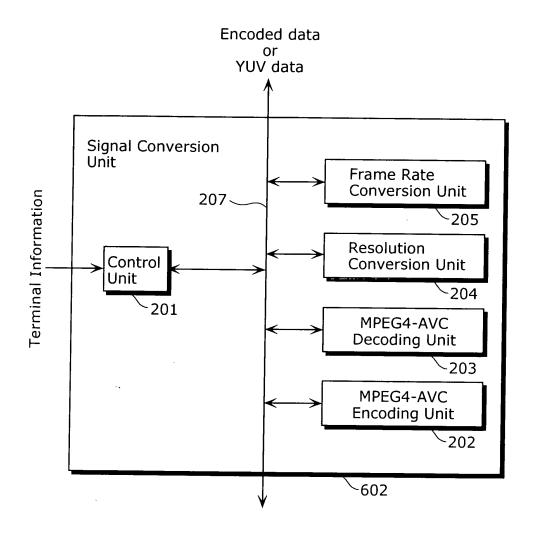


FIG. 22



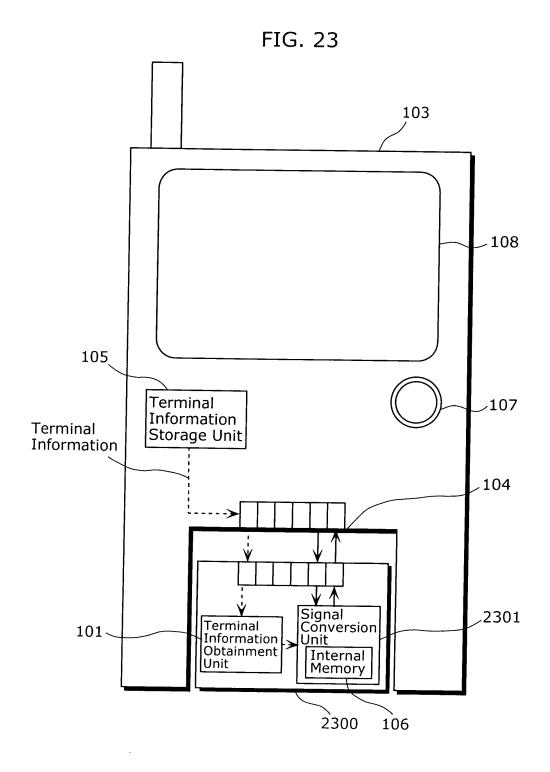


FIG. 24

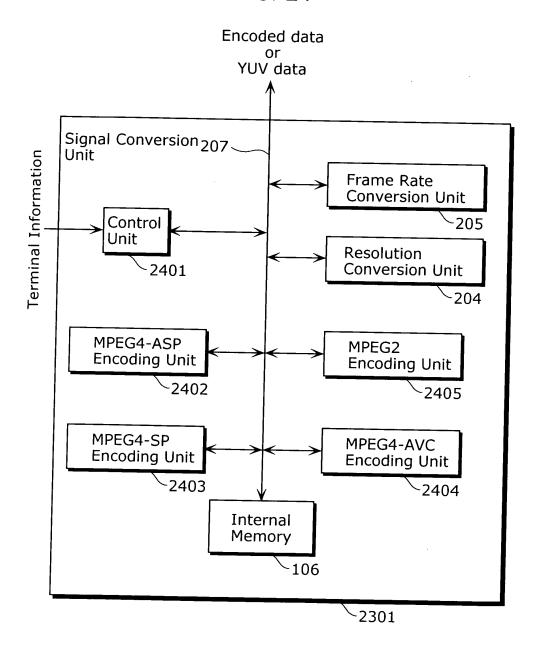


FIG. 25

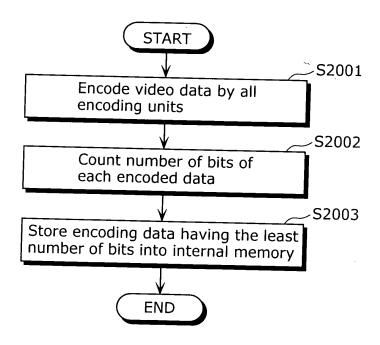


FIG. 26

